



PATENT APPLICATION  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

*Yoshiyuki Tanaka*

Appln. No.: 10/809,459

Group Art Unit:

Filed: March 26, 2003

Examiner:

For: SEMICONDUCTOR DEVICE, SYSTEM DEVICE USING IT, AND  
MANUFACTURING METHOD OF A SEMICONDUCTOR

STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir/Madam:

I, Satoshi Watanabe, of Ark Mori Building, 13F, 12-32, Akasaka 1-chome, Minato-ku, Tokyo 107-6028, Japan, hereby state that:

I well understand the Japanese and English languages and attached is an accurate English translation made by me of the Japanese specification in the above-identified U.S. patent application.

Date : December 22, 2005

Name :

Satoshi WATANABE

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